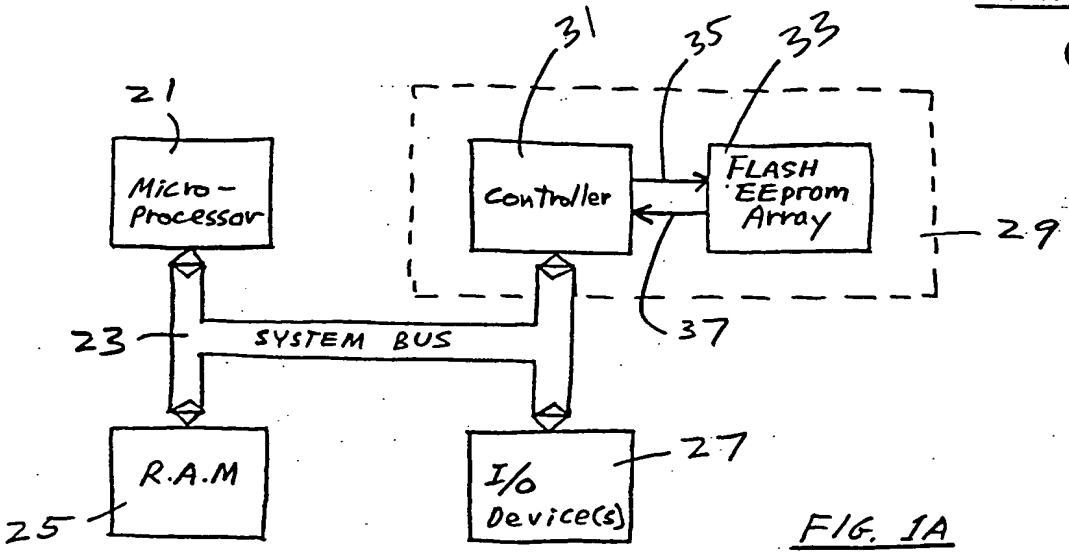

21
Micro-Processor
31
Controller
35
33
FLASH EEprom Array
29
37
23
SYSTEM BUS
R.A.M
25
I/O Device(s)
27

FIG. 1A

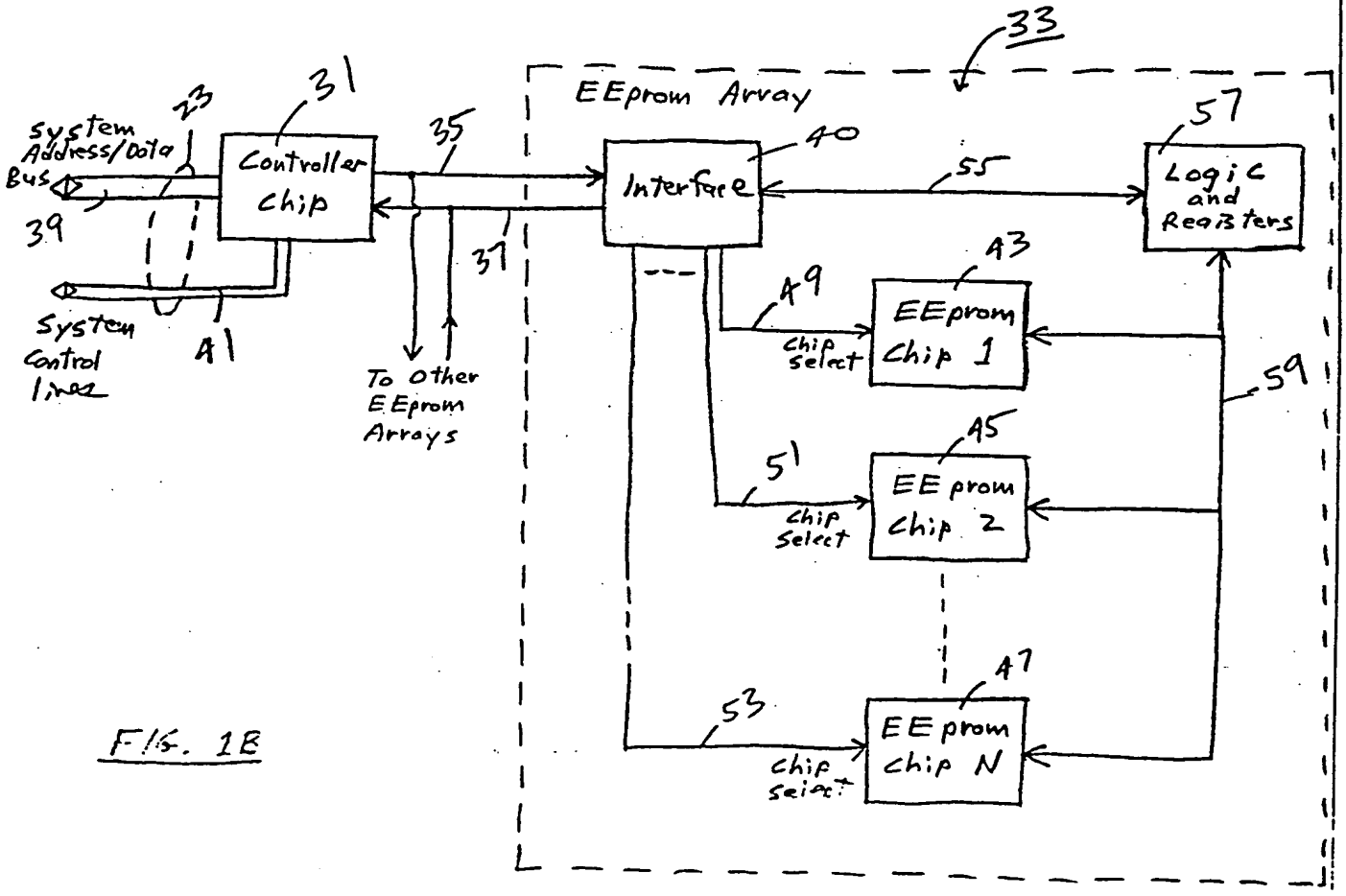

33
EEprom Array
System Address/Data Bus
23
39
31
Controller Chip
35
37
40
Interface
55
57
Logic and Registers
System Control lines
41
To other EEprom Arrays
43
49
chip select
EEprom Chip 1
59
45
51
chip select
EEprom Chip 2
47
53
chip select
EEprom Chip N

FIG. 1B

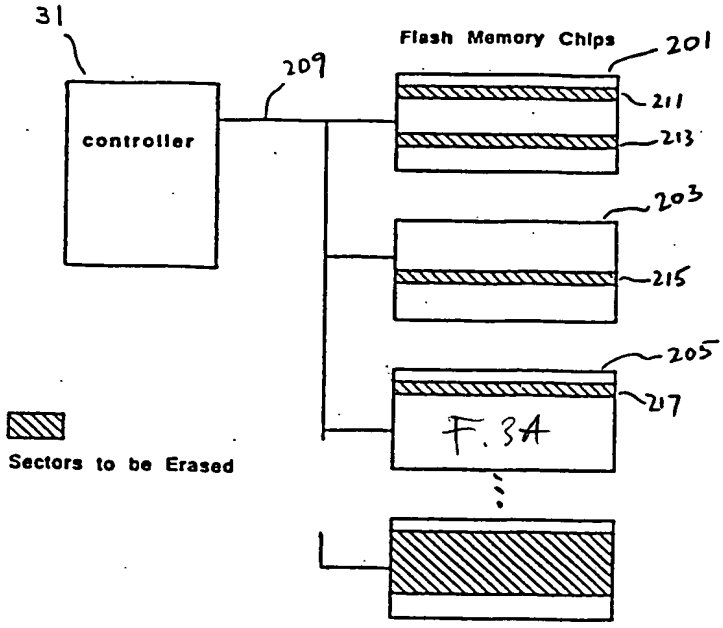


FIG_2

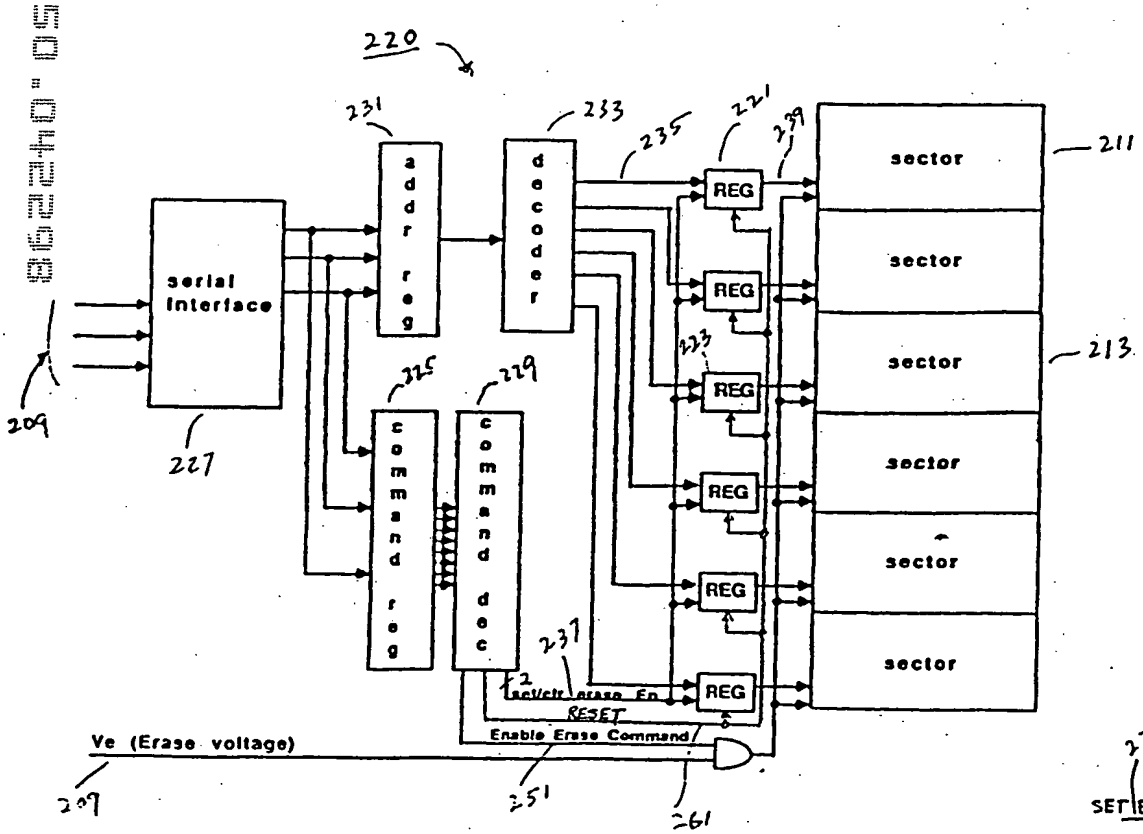


FIG_3A

Block ckt diagram (in the controller for implementing selective multiple sector erase

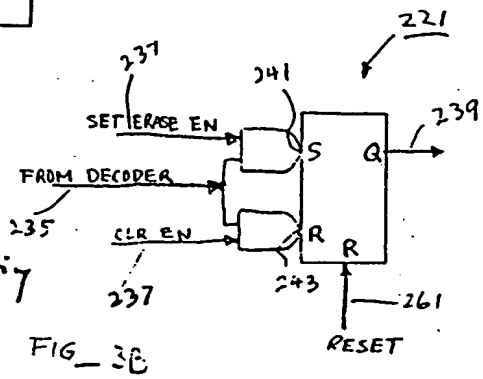


FIG_3B

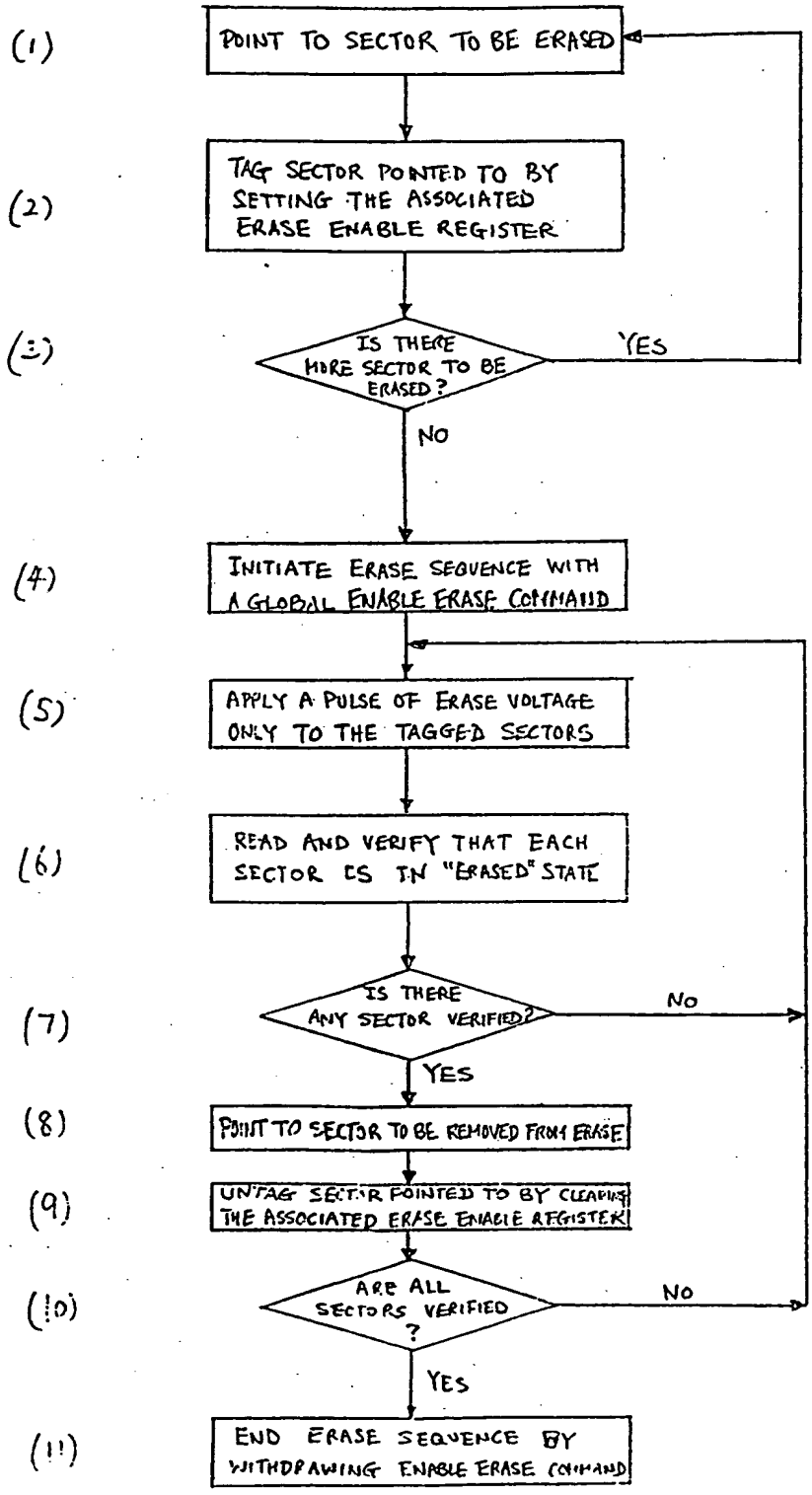
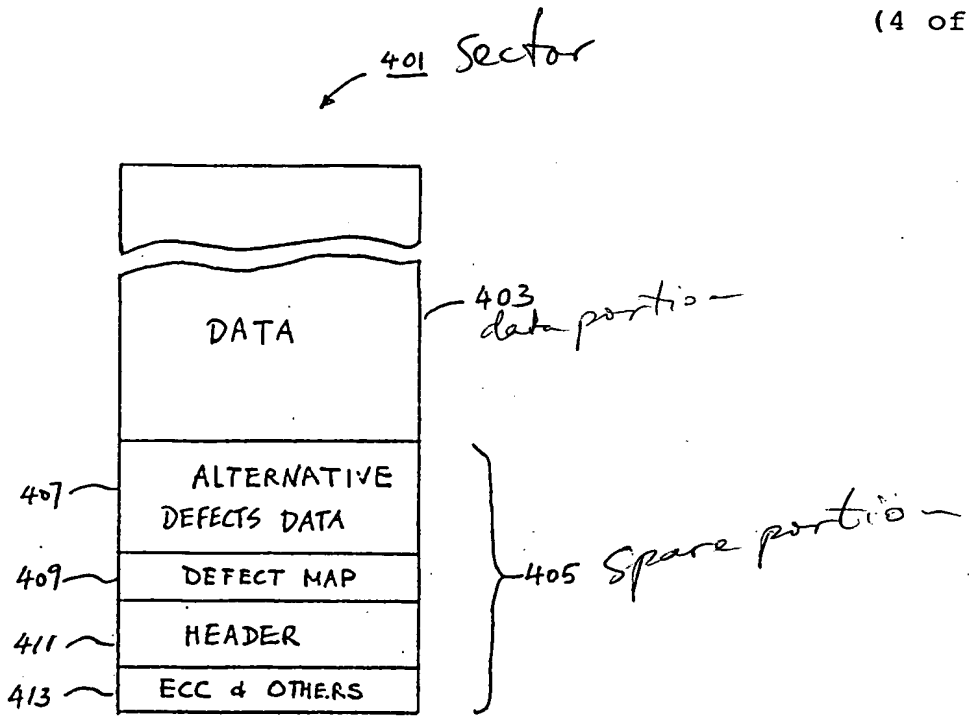


FIG_4

P. 11 – P. 14
How to erase
No Converting

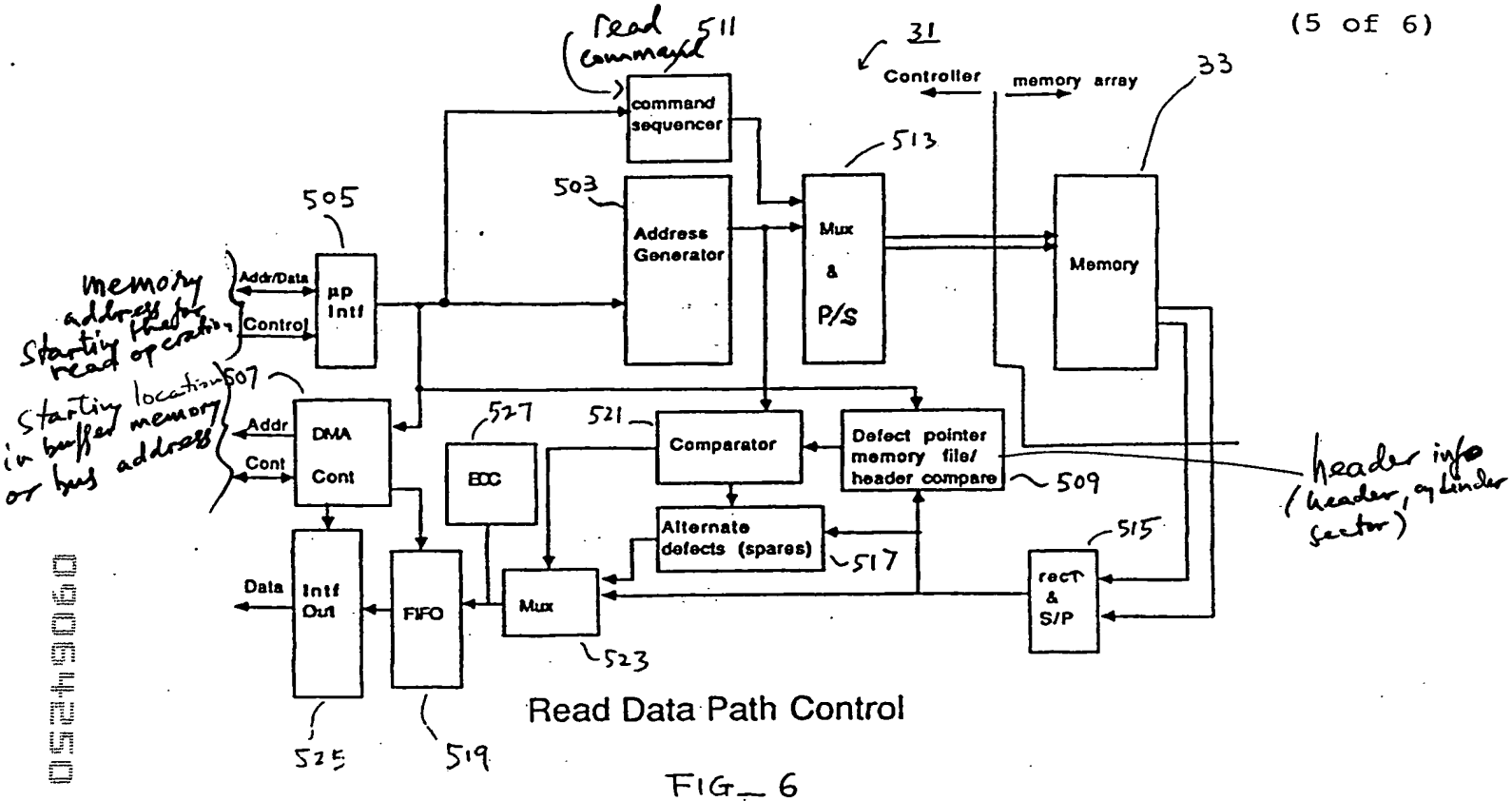
Doc code: 0205 Doc description: 0205



SECTOR PARTITION

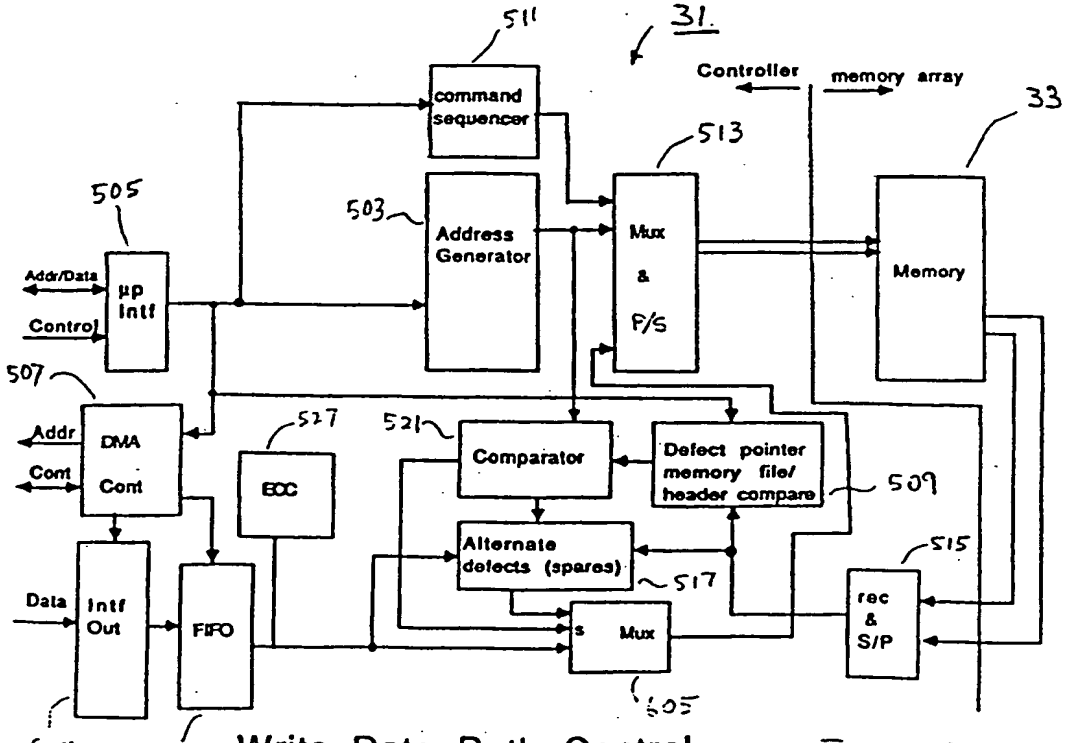
FIG_5 P.16

09025950.0042



Read Data Path Control

FIG_ 6



Write Data Path Control

FIG_ 7

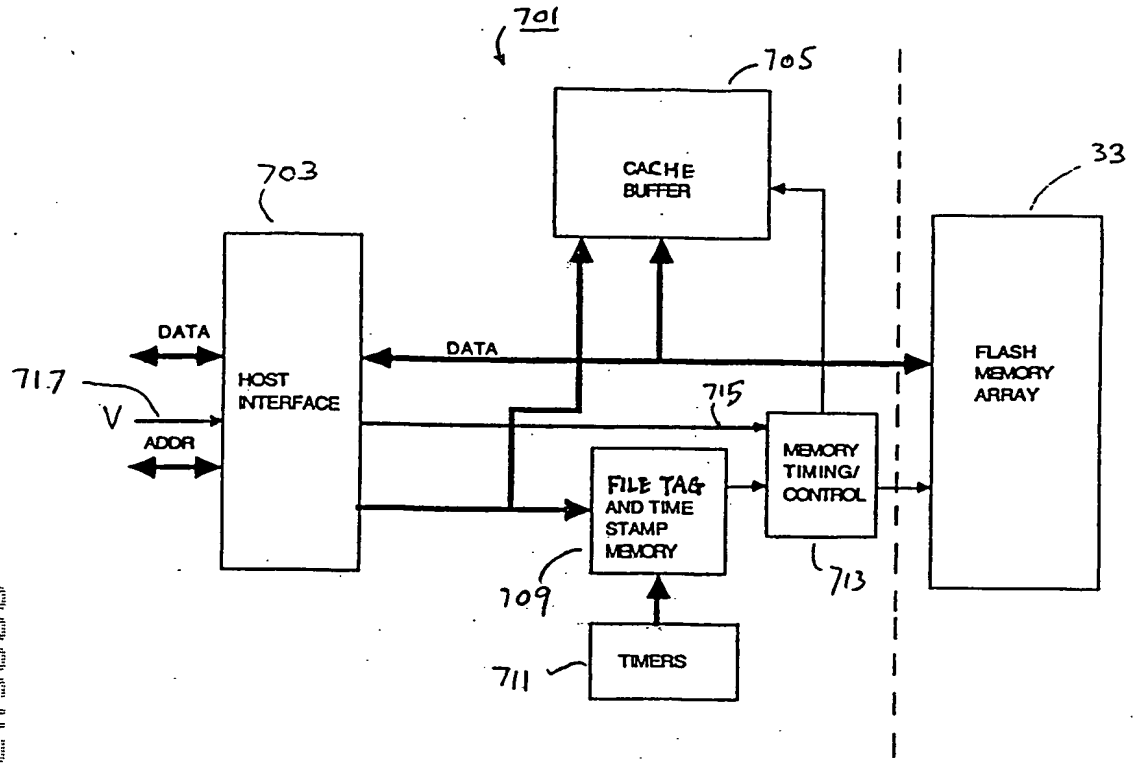


FIG. 8

SanDisk Corporation